

FIG. 1A PRIOR ART

LOOP: ld (BRANCH TARGET INSTRUCTION)

sub
brne Loop

FIG. 1B PRIOR ART

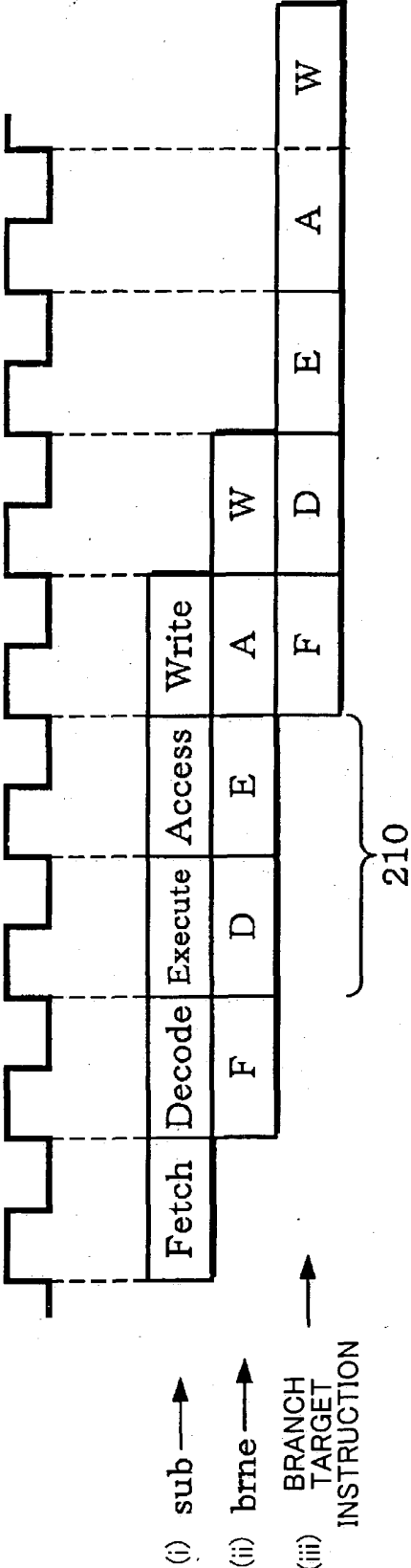


FIG. 2

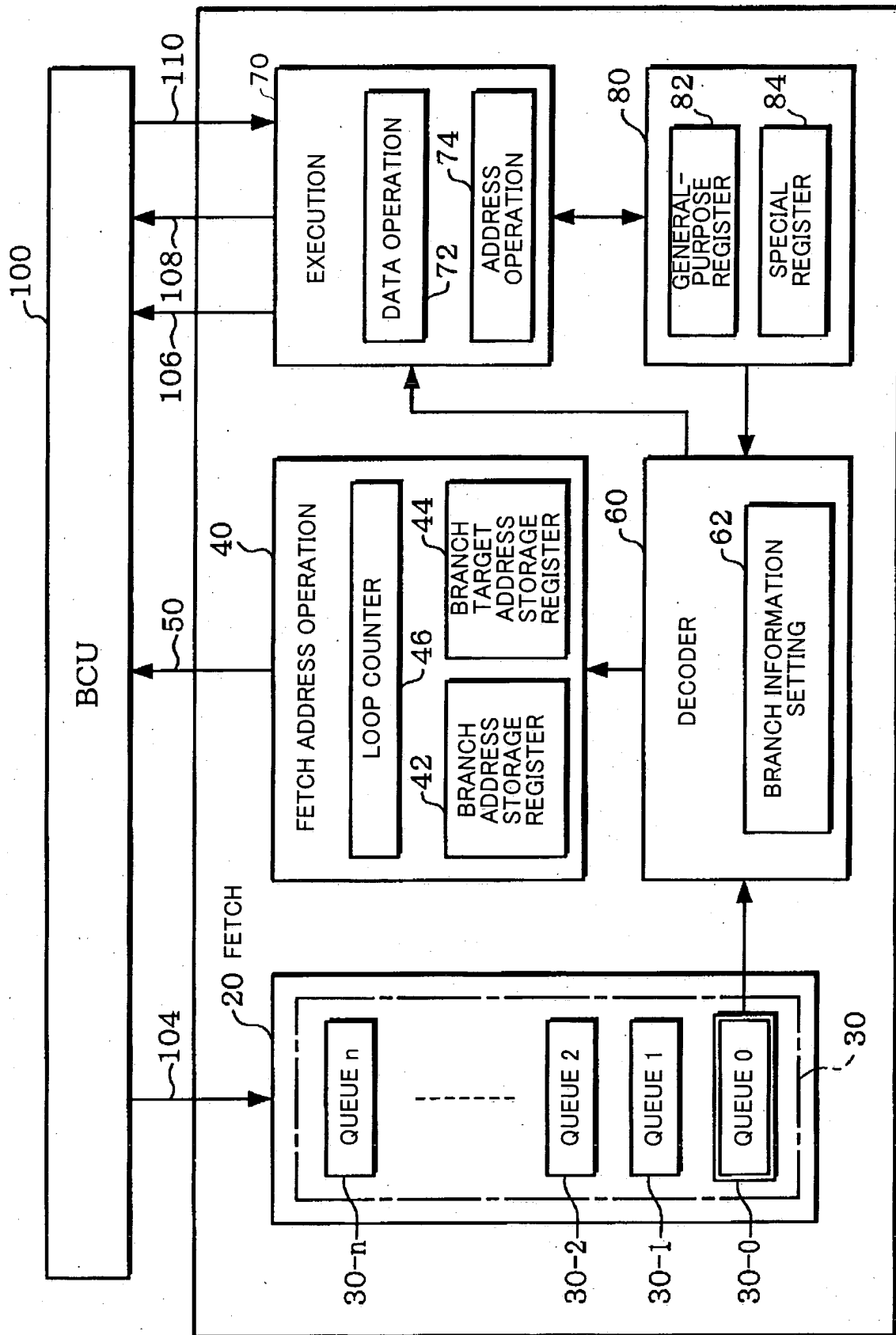


FIG. 3A

jprr x, y

FIG. 3B

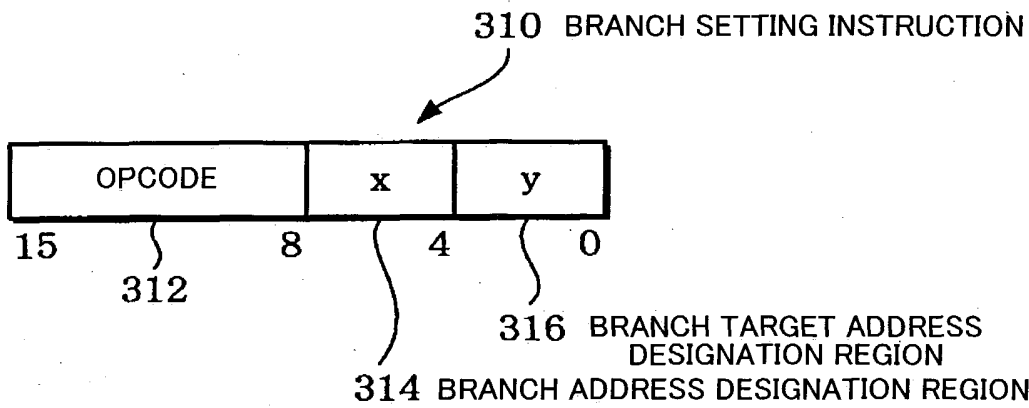


FIG. 3C

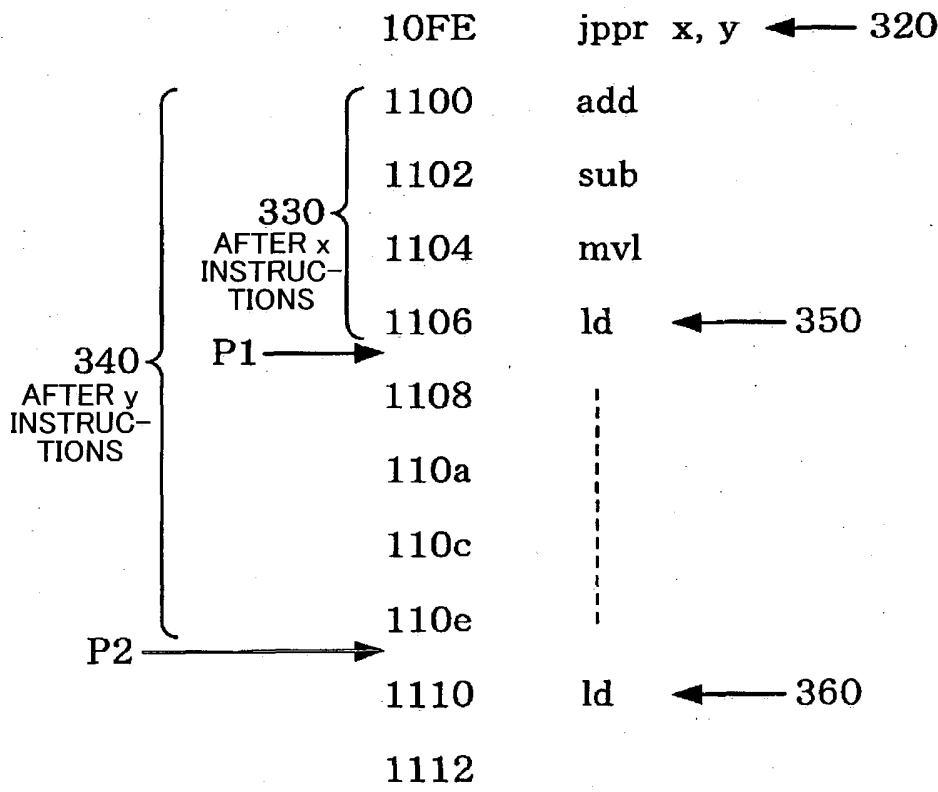


FIG. 4

40 FETCH ADDRESS OPERATION CIRCUIT

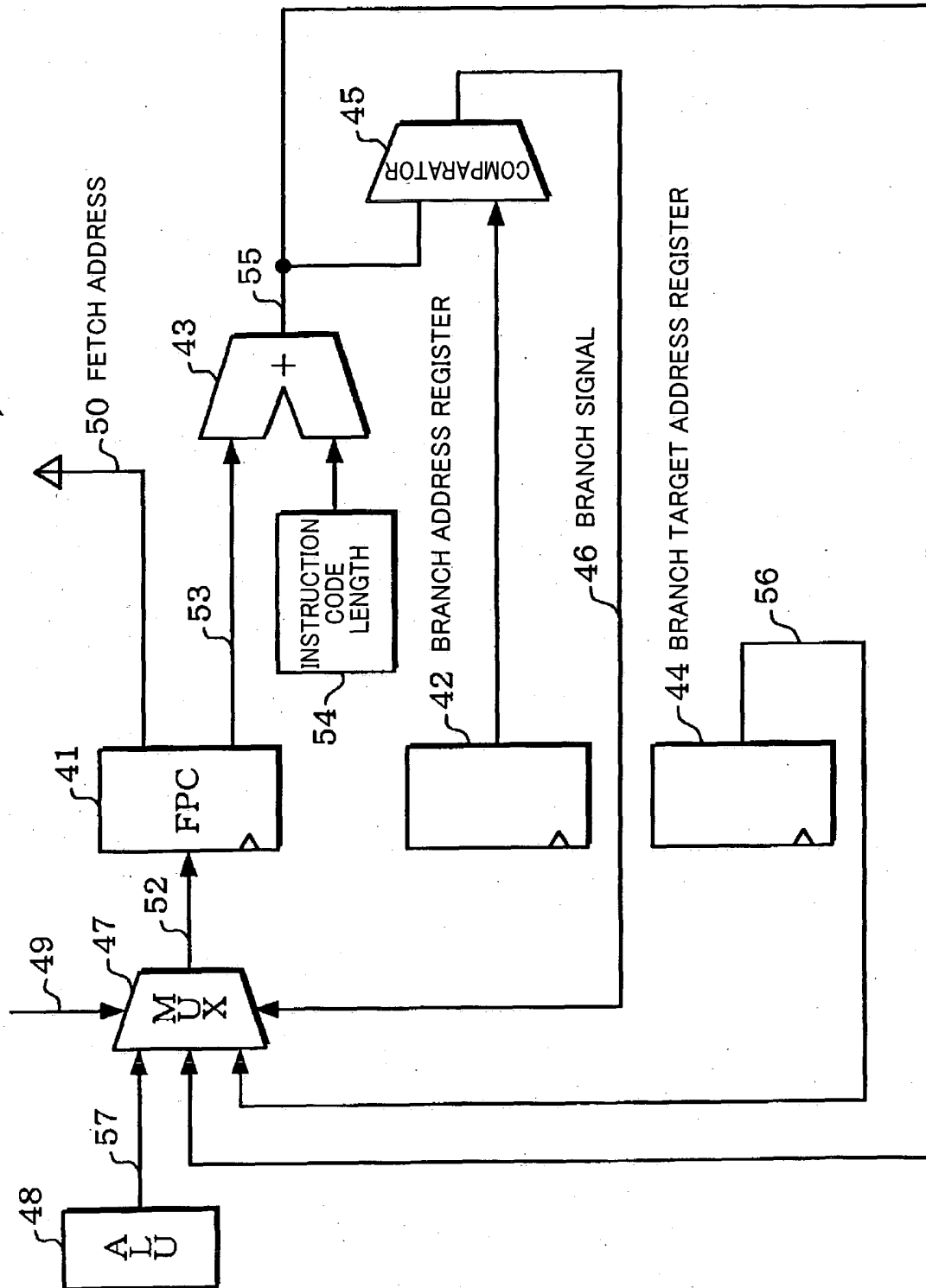


FIG. 5A

loop x, y, z

FIG. 5B

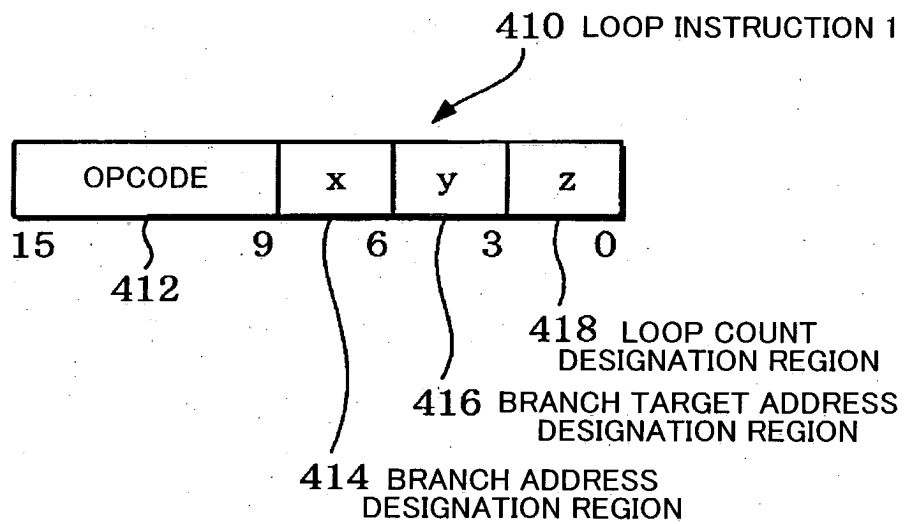


FIG. 5C

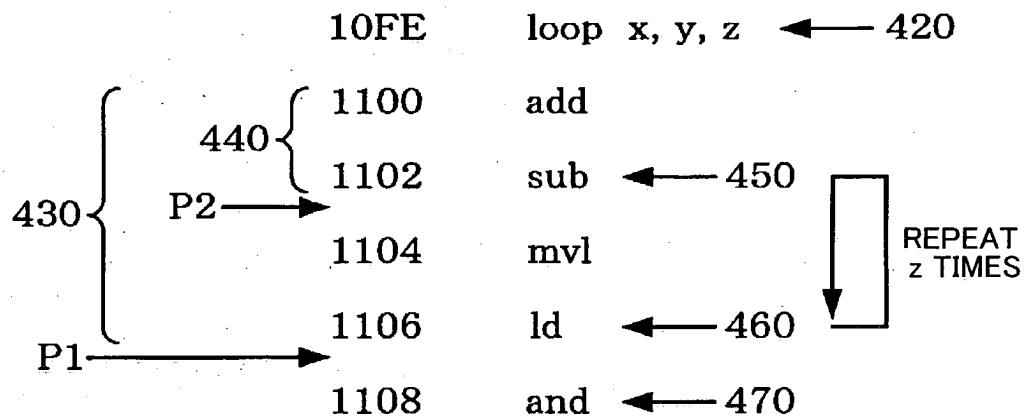


FIG. 6

40' FETCH ADDRESS OPERATION CIRCUIT

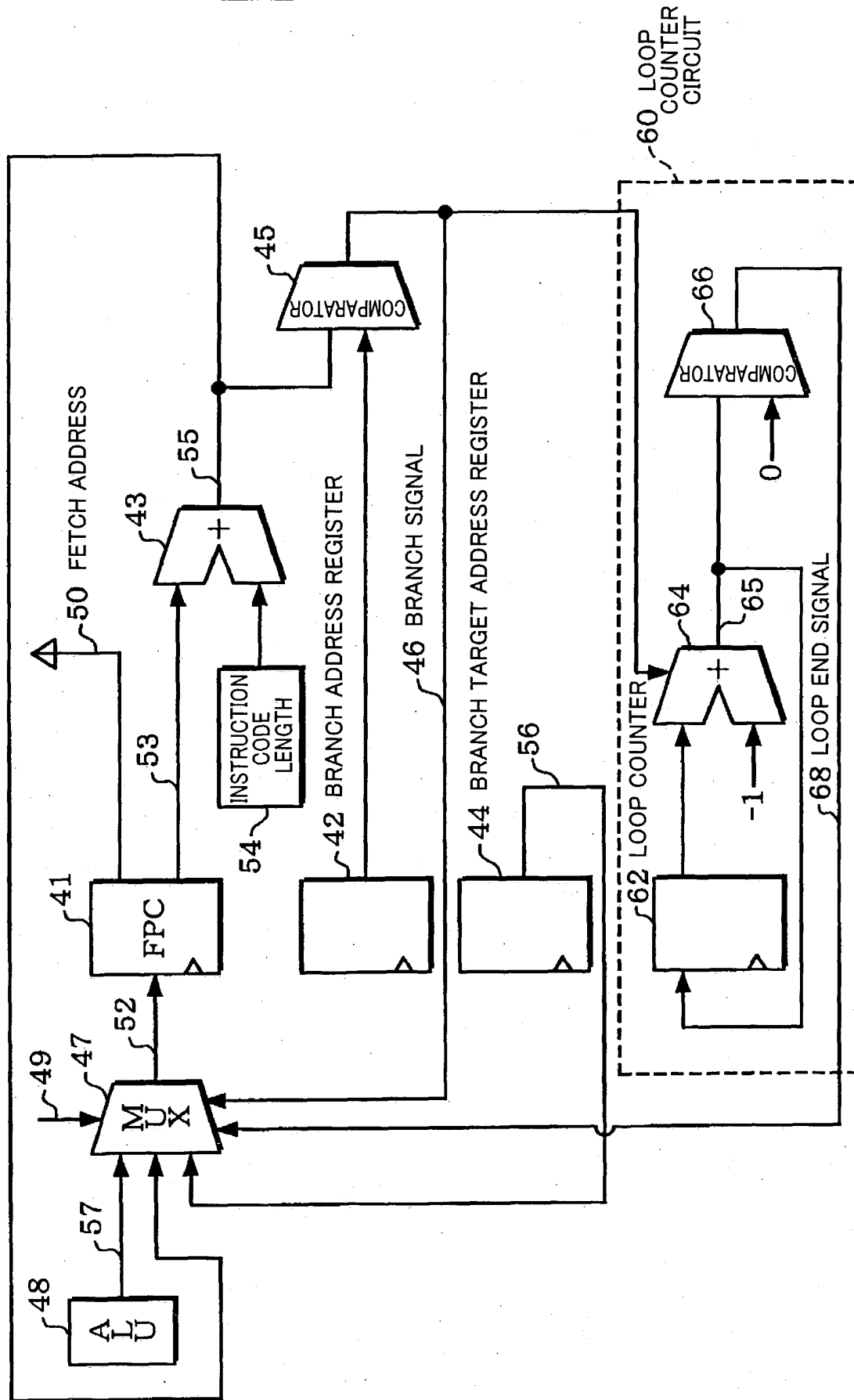


FIG. 7A

loop x, z

FIG. 7B

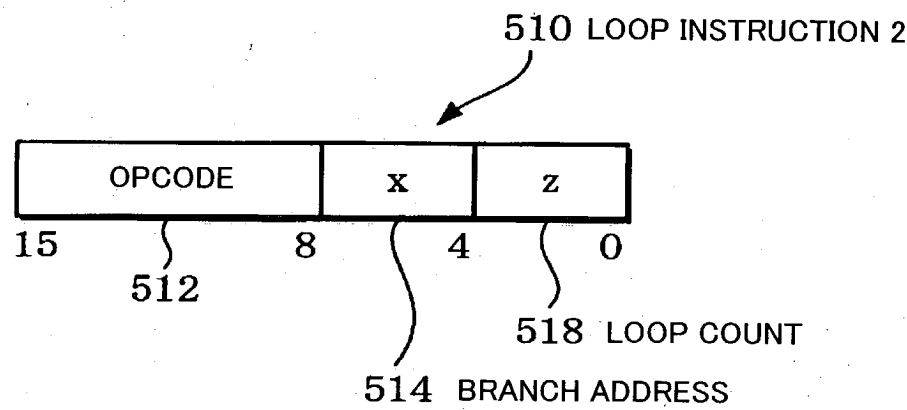


FIG. 7C

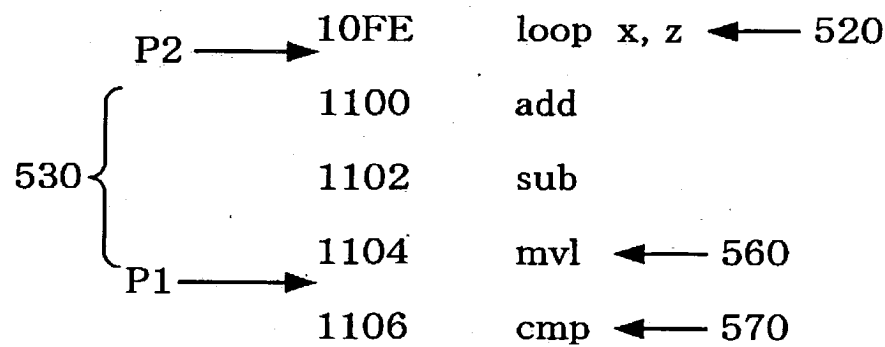


FIG. 8A

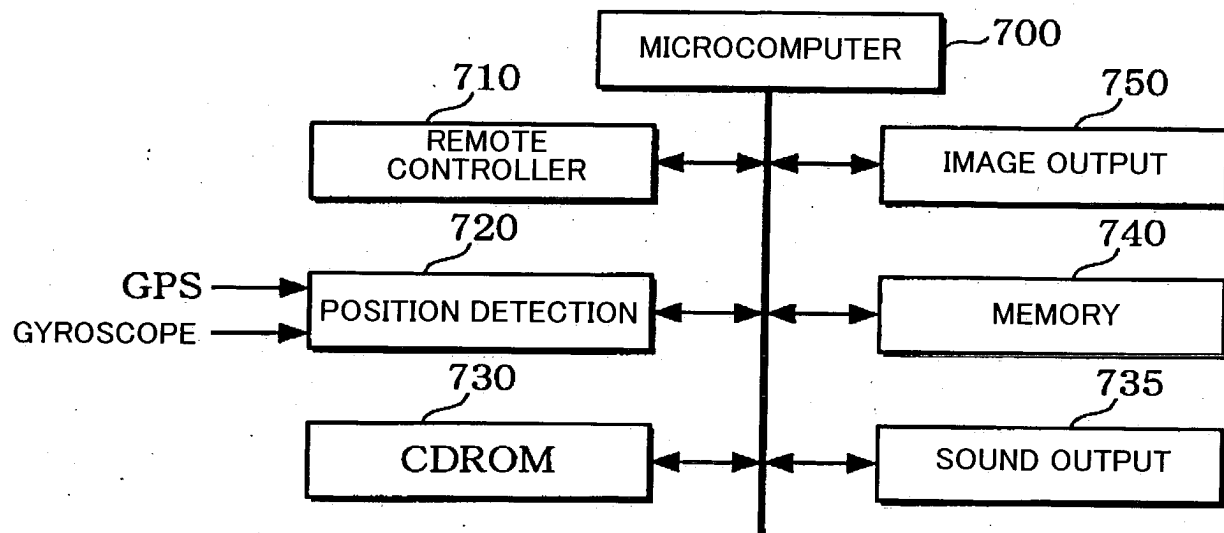


FIG. 8B

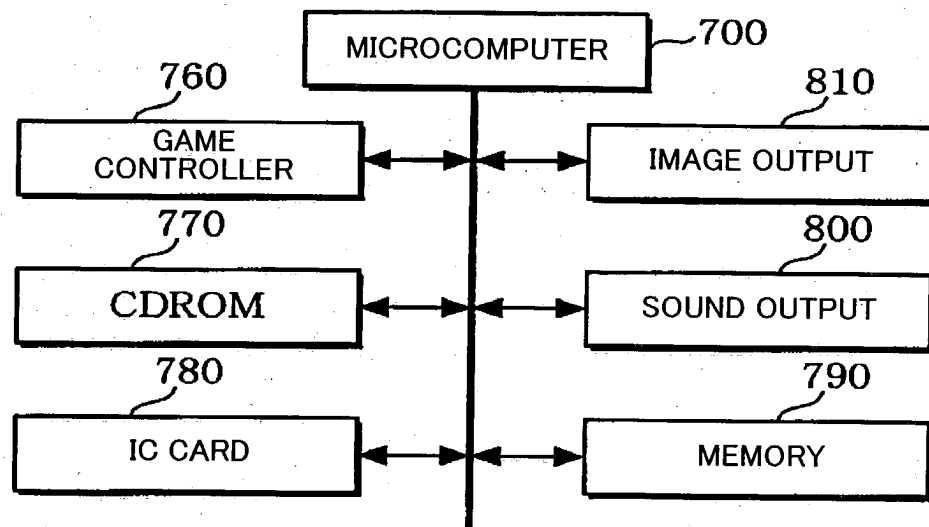


FIG. 8C

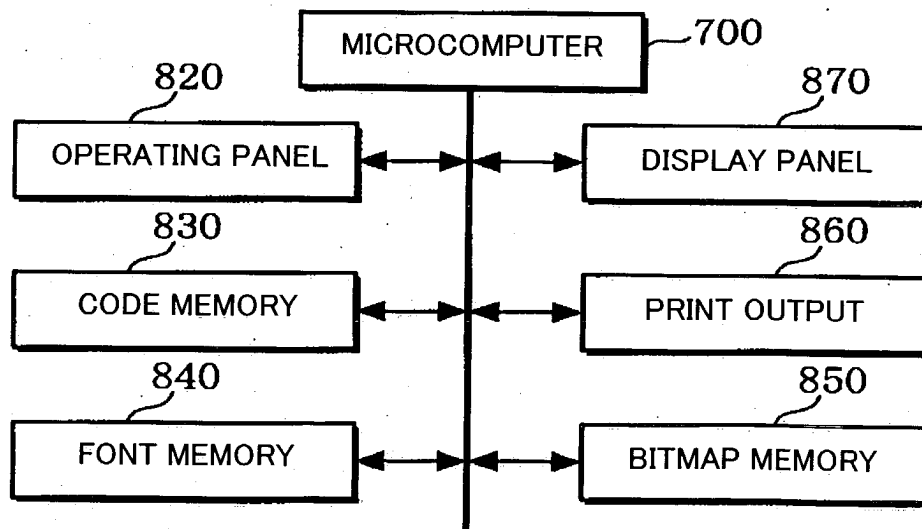


FIG. 9A

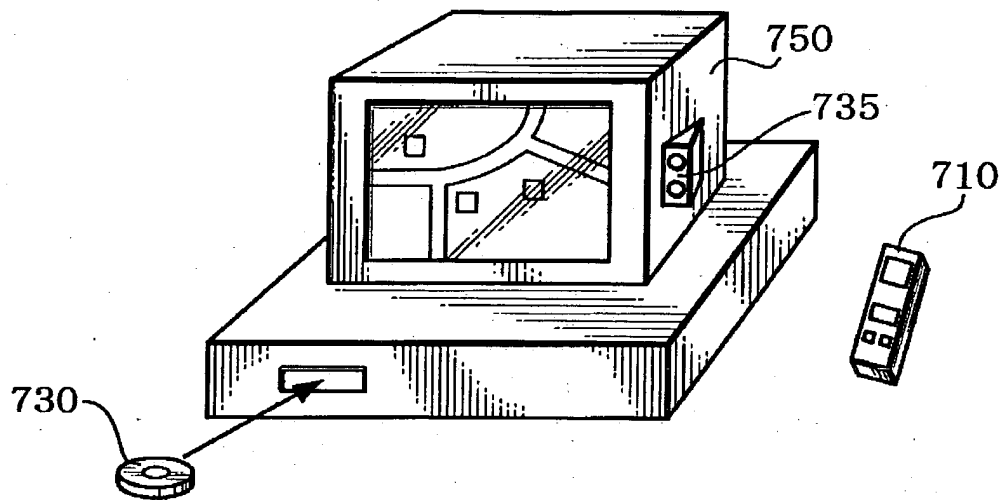


FIG. 9B

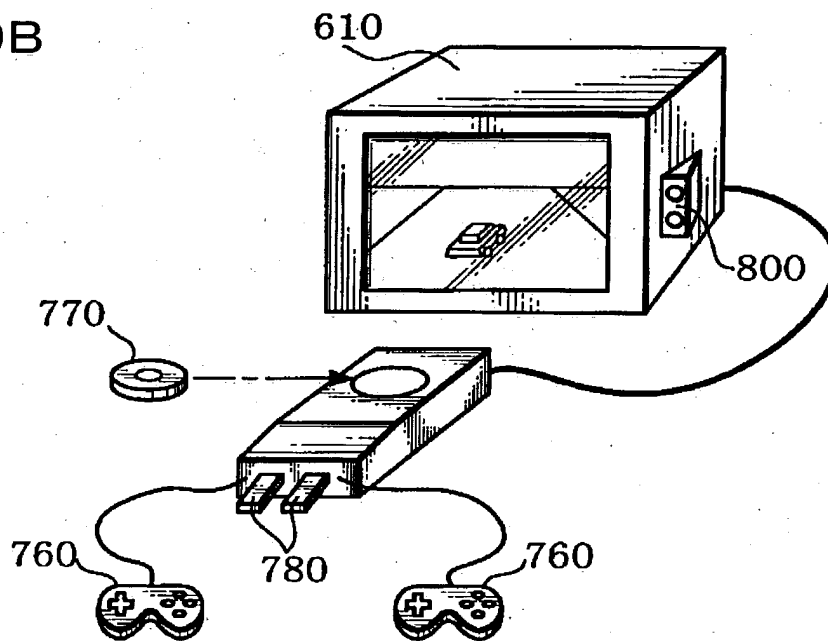


FIG. 9C

